

What is Claimed is:

1. A recording apparatus comprising:  
a microphone;  
an analog to digital converting circuit for converting an output signal from said microphone into a digital signal;  
a storage device for storing an output signal from said analog to digital converting circuit;  
an input device for at least entering a recording start and a recording end; and  
a controller for start and stop of writing into said storage device a digital signal outputted from said analog to digital converting circuit, said controller making a start of writing of a digital signal outputted from said analog to digital converting circuit into said storage device when a predetermined time interval has passed after said input device is operated.

2. A recording apparatus as claimed in Claim 1, wherein said apparatus further comprises a cabinet in which said microphone and said input device are arranged.

3. A recording apparatus as claimed in Claim 2, wherein said controller starts recording into said storage device when a predetermined time interval has passed after said input device is first-half operated; and stops recording of a digital signal from said analog to digital converting circuit into said storage device for a further predetermined time interval after

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said input device is second-half operated.

4. A recording apparatus as claimed in Claim 3, wherein said controller operates in such a manner that said input device restart writing of a digital signal from said analog to digital converting circuit into said storage device after said further predetermined time interval.

5. A recording apparatus as claimed in Claim 3, wherein said further predetermined time interval is equal to a time interval required for attenuation to an unhearable level of a noise generated upon operation of said input device.

6. A recording apparatus as claimed in Claim 3, wherein said predetermined time interval is equal to or longer than a time interval required for attenuation to an unhearable level of a noise generated upon operation of said input device.

7. A recording apparatus as claimed in Claim 3, wherein said input device is provided with at least one switch.

8. A recording apparatus as claimed in Claim 7, wherein said switch is a mechanical switch.

9. A recording apparatus as claimed in Claim 3, wherein said controller is provided with a buffer memory for temporarily storing a digital signal outputted from said analog

to digital converting circuit, and a digital signal read from said buffer memory is supplied to said storage device.

10. A recording apparatus as claimed in Claim 1, wherein said storage device has at least two files and output signals from said microphone which have been converted into digital signals by said analog to digital converting circuit are written into one of said files specified by said input device.

11. A reproducing apparatus comprising:

a storage device into which at least one dynamic data is written;

time setting means for setting a time;

a memory for storing a time data set by said time setting means; and

a controller for reading a dynamic data from said storage device according to a time data stored in said memory.

12. A reproducing apparatus as claimed in Claim 11, wherein said controller carries out writing into said memory, correlating the time data specified by said time setting means with a dynamic data stored in said storage device, and reading from said storage device the dynamic data corresponding to said specified time.

13. A reproducing apparatus as claimed in Claim 12, wherein said controller is provided with a clock section, and

when said clock section coincides with a specified time data stored in said memory, said controller reads out a dynamic data from said storage device.

14. A reproducing apparatus as claimed in Claim 11, wherein said reproducing apparatus is provided with a conversion circuit for converting a dynamic data read from said storage device, into an analog signal.

15. A reproducing apparatus as claimed in Claim 14, wherein a plurality of dynamic data are written into said storage device; and said plurality of dynamic data are written by said controller into at least two separate files provided in said storage device;

    said apparatus being provided with an input device, which is capable of specifying a dynamic data to be read out selectively from said storage device.

16. A recording and/or reproducing apparatus comprising:  
    a microphone;  
    an analog to digital converting circuit for converting an output signal from said microphone, into a digital signal;  
    a semiconductor memory for storing an output signal from said analog to digital converting circuit;  
    a digital to analog converting circuit for converting a digital signal read from said semiconductor memory, into an analog signal;

an input device for entering at least a recording start, a recording end, and a reproduction start;

a controller for controlling, according to an input from said input device, writing of a digital signal from said analog to digital converting circuit, into said semiconductor memory, and reading of a digital signal from said semiconductor memory; and

a cabinet in which said microphone, said analog/digital conversion circuit, said digital/analog conversion circuit, and said input device are arranged.

17. A recording and/or reproducing apparatus as claimed in Claim 16, wherein said semiconductor memory is provided with at least two files; and output signals from said microphone are converted by said analog to digital converting circuit into a plurality of digital signals, which are written in a file specified by said input device.

18. A recording and/or reproducing apparatus as claimed in Claim 17, wherein said controller selectively reads out from said storage device a dynamic data specified by said input device and supplies the data to said digital to analog converting circuit.

19. A recording and/or reproducing apparatus as claimed in Claim 17, wherein

said controller controls to start writing a digital signal

from said analog to digital converting circuit into said semiconductor memory and to stop writing into said semiconductor memory according to an input from said input device; and

when said input device is operated and a predetermined time interval has passed, said controller makes starts writing into said semiconductor memory a digital signal outputted from said analog to digital converting circuit.

20. A recording and/or reproducing apparatus as claimed in Claim 19, wherein

    said controller is provided with a switch for entering said recording start; and

    said controller starts recording into said storage device when said predetermined time interval has passed after said switch is first-half operated, and terminates writing into said storage device a digital signal from said analog to digital converting circuit when a further predetermined interval has passed after said switch is second-half operated.

21. A recording and/or reproducing apparatus as claimed in Claim 20, wherein said controller starts writing into said storage device a digital signal from said analog to digital converting circuit when said further predetermined time interval has passed after said switch is second-half operated.

22. A recording and/or reproducing apparatus as claimed

in Claim 20, wherein said further predetermined time interval is equal to a time required for attenuation to an unhearable level of a noise which is generated when said switch is operated.

23. A recording and/or reproducing apparatus as claimed in Claim 19, wherein said predetermined time interval is equal to or longer than a time required for attenuation to an unhearable level of a noise which is generated when said switch is operated.

24. A recording and/or reproducing apparatus as claimed in Claim 20, wherein said controller is provided with a buffer memory for temporarily storing a digital signal outputted from said analog to digital converting circuit so that a digital signal read from said buffer memory is supplied to said storage device.

25. A recording and/or reproducing apparatus as claimed in Claim 17, wherein

    said input device is provided with time setting means for setting a time;

    said apparatus further comprises a further memory for storing a time data which has been set by said time setting means; and

    said controller reads out a digital signal from said semiconductor memory according to the time data stored in said

further memory.

26. A recording and/or reproducing apparatus as claimed in Claim 25 wherein said controller writes a time data which has been set by said time setting means, into said further memory corresponding to a digital signal stored in said semiconductor memory; and, according to said time which has been set, reads out a digital signal corresponding to said time from said semiconductor memory.

27. A recording and/or reproducing apparatus as claimed in Claim 26, wherein said controller is provided with a clock section, and when said clock section coincides with a time set data stored in said further memory, said controller reads out a digital signal from said semiconductor memory.

TOEGLIO SETTIMANA